

**DYNAMIC PROCESSOR CONFIGURATION
AND POWER-UP**

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ABSTRACT OF THE DISCLOSURE

A dynamic processor configuration and power-up programs a processor's fuse block with configuration signals during processor manufacturing. The processor
10 configuration signals include a core voltage identifier and a system bus frequency identifier. When power is applied to the platform, a control signal is used to prevent power-up of the platform's processor related circuitry. While the platform awaits full power-up, the fuse block is powered up. When the fuse block is powered up, the control signal is used to allow the configuration signals to be read from the fuse block.
15 The processor is configured with core voltage and system bus frequency based on the values read from the fuse block. The platform then performs its boot-up sequence.